



PID Control on PIC16F161X by using a PID Peripheral

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INTRODUCTION

Several products in the PIC[®] device family have many features that can be applied to motor control or power supplies, including the Proportional Integral Derivative (PID) control module. A PID control mode is implemented in this module to do a discrete-time PID or PI, PD, etc. control. This technical brief describes briefly how the module works, how to use this module in users' applications, and what should be observed.

BASIC PRINCIPLE

In control theory, a PID controller is an algorithm that uses the present error (proportional), the sum of the present and all previous errors (integral), and the difference between the present and previous change (derivative) to correct errors and provide stability in a system. It provides feedback to a system through a series of iterations, using the present error as well as previous errors to calculate a new input to the controller.

In a continuous-time system, the PID controller can be transferred from time domain to s-domain by using a Laplace Transform. The PID output equation is shown below.

$$\frac{C(s)}{E(s)} = K_p + \frac{K_i}{s} + K_d s$$

Next, a Finite Difference Method Z-transform is implemented on the continuous-time output equation.

The method is simply replacing 's' with (1-z)⁻¹/T, and T is the sampling period of the discrete-time system. The discrete-time PID output equation is shown below.

$$\frac{C_{o(z)}}{E(z)} = \frac{\left(K_p + K_i T + \frac{K_d}{T}\right) + \left(-K_p^{\ o} - \frac{2K_d}{T}\right)z^{-1} + \left(\frac{K_d}{T}\right)z^{-2}}{1 - z^{-1}}$$

Where:

$$K1 = K_p + K_i T + \frac{K_d}{T}$$

$$K2 = -K_p - \frac{2K_d}{T}$$

$$K3 = \frac{K_d}{T}$$

Then the output equation can be written as:

$$\frac{C_{o(z)}}{E(z)} = \frac{K1 + K2 \cdot z^{-1} + K3 \cdot z^{-2}}{1 - z^{-1}}$$

At last, as z^{-1} represents one delay in time, the output equation is transferred into a difference equation in time domain by replacing z^0 with [k], z^{-1} with [k-1], and z^{-2} with [k-2]:

$$C_0[k] = C_0[k-1] + {}^0K_1e[k] + {}^0K_2e[k-1] + {}^0K_{3(2)}e[k-2]$$

Where $c_0[k]$ is the current PID output, $c_0[k-1]$ is the previous PID output, and K1, K2 and K3 are the parameters of the current error, the previous error and the one before the previous error.

Based on the difference equation, the module is designed as shown in Figure 1.

IMPLEMENTATION

Inputs

To implement this module, three major inputs need to be calculated or determined, which are the set point value, the PID input value (feedback value), and the K1, K2 and K3 values.

The set point value is designed by users based on their own application, and it should be a 15-bit signed or a 16-bit unsigned integer. Users are free to design their set point, such as a CCP value of a PWM waveform, or an ADC value of the desired voltage. The set point value is written into the PIDxSET register.

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The PID input value, also known as the feedback, should be measured by users on the process they want to control (motor, power supply, heater, etc.). The measured value is stored in the PIDxIN register and should be the exact same type as the set point value. For instance, if the set point value is a 16-bit ADC value, the PID input should also be a 16-bit ADC value that users measure from their application.

To calculate K1, K2 and K3, two things need to be designed by the user. One is the $\rm K_p$, $\rm K_i$ and $\rm K_d$ in the users control systems. Another is the sampling period T of the system. Once these are designed, the K1, K2 and K3 can be calculated. Since the sampling period is taken into account, the calculated results of K1, K2 and K3 are usually not integers. The user needs to scale them up to three 15-bit signed integers, and then store them in the PIDxK1, PIDxK2 and PIDxK3 registers, respectively. Please note that the scale factors for K1, K2 and K3 should be the same, and the PID output value should be scaled back down by the same factor in order to be used as the real output of the PID controller. All the scaling is done in software by the user.

Overall, in order to set up the module, the user needs to set the PIDxK1, PIDxK2, PIDxK3 and PIDxSET registers as inputs. The module is able to do the PID calculation by itself.

Operating

After setting up the set point value and the K1, K2 and K3 values, the PID module is ready to operate. Please note that the calculation is started when PIDxINL (the lower byte of PIDxIN) is written, so any changes to any register has to be made before the low value of the PIDxIN register is updated.

A BUSY flag of the PIDxCON register is set to '1' when the module is busy calculating, and the flag will return to '0' when the module is done calculating. A PIDxDIF interrupt flag is also triggered after one successful calculation. The user can read the value in PIDxOUT after the BUSY flag returns to '0' or the PIDxDIF interrupt is triggered, and scale the value down by the same factor used when scaling up the K values to get the real PID output of this iteration.

Sampling Period

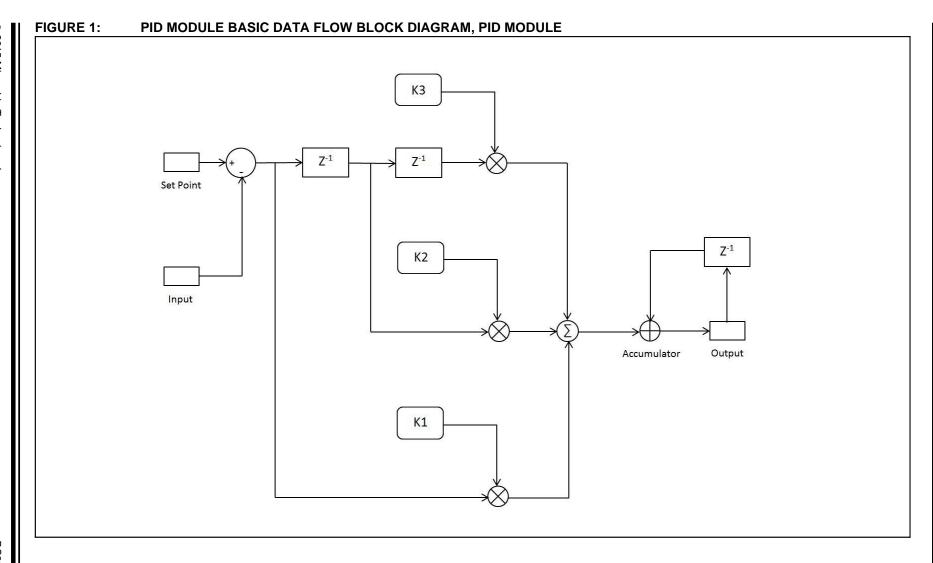
Since it's a discrete-time system, a sampling rate should be determined. The sampling period of the system should be the time from when one PID input is loaded until the next PID input is loaded. Please note that the sampling period is not the time of one PID calculation, since users can implement their own algorithms or programs in one iteration and the PID module calculation could only take up a small amount of time. Users can choose the sampling period according to their needs, but within the limitation of the module and the system (accumulator size, K1, K2, K3 registers' size, etc.).

Accumulator

A 35-bit accumulator is used to accumulate the PID output. Please note that the accumulator may overflow when the error of the PID controller (set point, PID input) is too big or accumulates too much. There are certain circumstances that the accumulator may overflow easily. One is choosing a rather fast sampling rate, which makes the error add up quickly in a short time. One is integral windup, where there is a rapid big change of the set point, which results in a large error. A system with a long response time could also cause the accumulator to overflow since the error does not reduce fast with a long response time.

CONCLUSION

The PID module can be used in many control systems applications. Since the module does the multiplication and accumulation for the user, it saves users a lot of time and allows for implementation of more operations based on their own applications. It takes only nine instruction cycles for the module to do a PID calculation, while it takes around 1000 instruction cycles by using software to do the same calculation. Therefore, there is a huge efficiency improvement using the PID module.



APPENDIX A: CODE FOR IMPLEMENTING THE MATH ACCELERATOR

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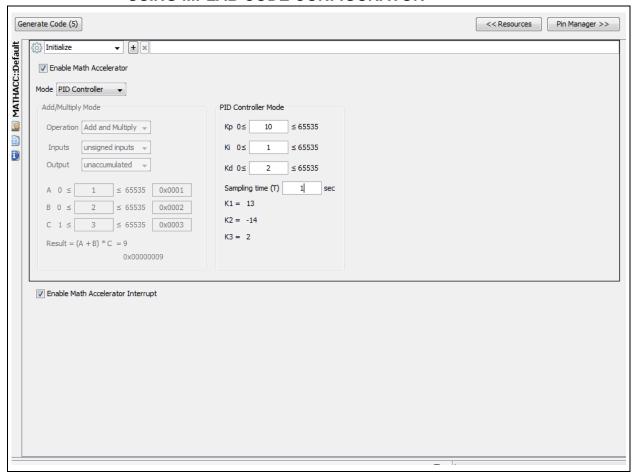
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```
/**
* Setup the PID mosdule to do a PID calculation.
PID1ACCU = 0;
                      // Clear the Accumulator (PID1ACCU)
PID1ACCHH = 0;
                      // Clear the Accumulator (PID1ACCHH)
PID1ACCHL = 0;
                      // Clear the Accumulator (PID1ACCHL)
PID1ACCLH = 0;
                      // Clear the Accumulator (PID1ACCLH)
PID1ACCLL = 0;
                      // Clear the Accumulator (PID1ACCLL)
PID1CON1bits.MODE = 0b101;
                      // PID mode
PID1CON1bits.EN = 1;
                      // PID module is enabled (PID1CON1)
* Calculate K1, K2, K3 based on Kp, Ki, Kd and the sampling rate
kp = 912;
ki = 9000;
kd = 41;
// Sampled at 256 Hz. Sampling period is 3.9 ms
K1 = kp + (ki >> 8) + (kd << 8); // K1 = Kp + Ki*T + Kd/T
K2 = -kp - (2 * (kd << 8)); // K2 = -Kp - 2*Kd/T
K3 = (kd << 8); // K3 = Kd/T
/**
* Load in the values
PTD1K1 = K1;
PID1K2 = K2;
PID1K3 = K3;
PID1SET = 100;
PID1INH = 0; //
PID1INL = 0; // This starts the math execution (PIDxINL)
while(PID1CONbits.BUSY); // Wait for the module to complete
result = PID10UT; // Load the result
```

APPENDIX B: SCREEN SHOT OF HOW TO INITIALIZE THE PID MODULE BY USING MPLAB CODE CONFIGURATOR



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