

IGBT D3 PAK Package Outline Dimension Update

Clarifications/Corrections to the Datasheet:

In the device datasheets listed below, the following clarifications and corrections should be noted. Please check the Microchip website for any existing issues.

TABLE 1: AFFECTED PART NUMBERS AND DOCUMENTS

Part Number	Part Description	Datasheet Number ⁽¹⁾	Datasheet Revision Level ⁽¹⁾
APT80GA90S	IGBT PT MOS 8 Single 900 V 80 A TO-268	052-6324	C

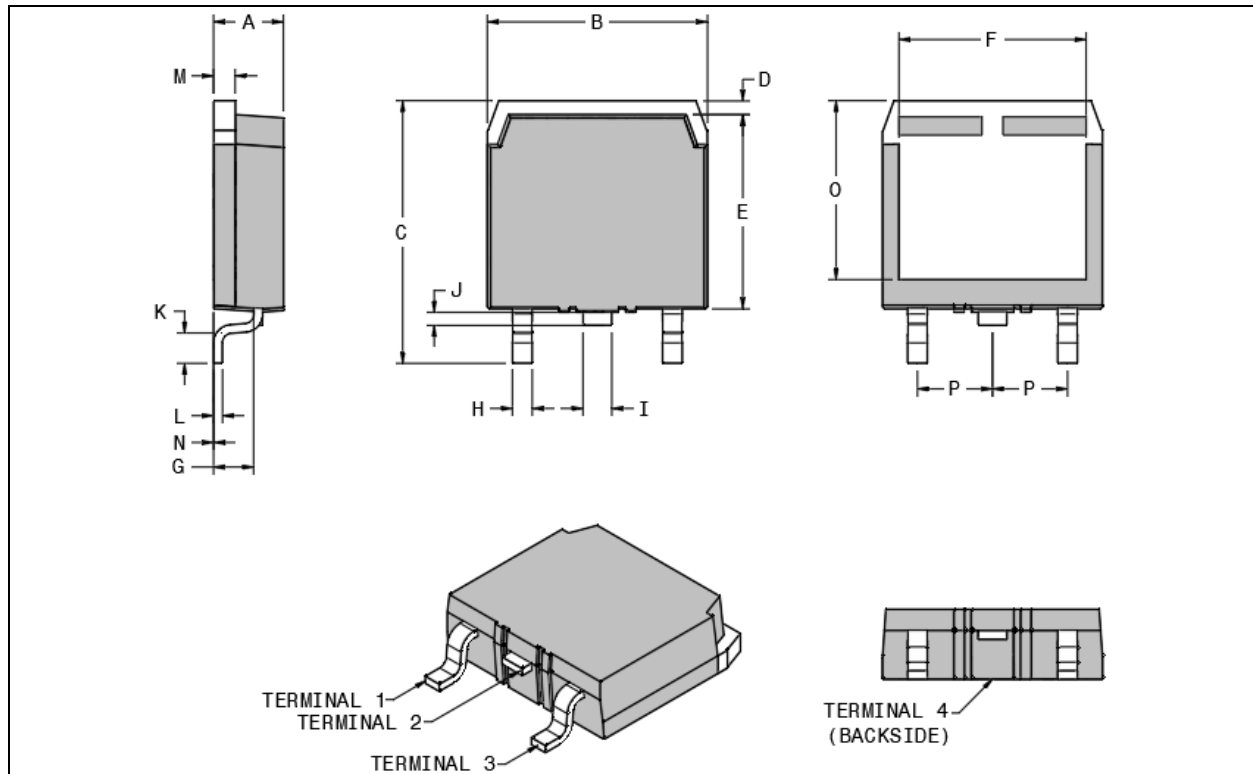
Note 1: THE NUMBER AND REVISION LEVEL IS REFERENCED IN THE MARGIN OF THE DATASHEET.

SUMMARY OF CHANGES

The D3 PAK Package Outline is missing one dimension. In the dimensional diagram below, this dimension (K) is included.

Terminal 4 (backside) is incorrectly labeled as “Drain”. In the dimensional diagram below, Terminal 4 is correctly labeled as “Collector”.

FIGURE 1: D3 PAK PACKAGE OUTLINE DRAWING



IGBT D3 PAK

TABLE 2: D3 PAK DIMENSIONS AND TERMINALS

Symbol	Min	Max	Min	Max
	[mm]		[inch]	
A	4.90	5.10	0.193	0.201
B	15.85	16.20	0.624	0.638
C	18.70	19.10	0.736	0.752
D	1.00	1.25	0.039	0.049
E	13.80	14.00	0.543	0.551
F	13.30	13.60	0.524	0.535
G	2.70	2.90	0.106	0.114
H	1.15	1.45	0.045	0.057
I	1.95	2.21	0.077	0.087
J	0.94	1.40	0.037	0.055
K	2.40	2.70	0.094	0.106
L	0.40	0.60	0.016	0.024
M	1.45	1.60	0.057	0.063
N	0.00	0.18	0.000	0.007
O	12.40	12.70	0.488	0.500
P	5.45 BSC		0.215 BSC	
Terminal 1	GATE			
Terminal 2	COLLECTOR			
Terminal 3	EMITTER			
Terminal 4	COLLECTOR			

Note 1: Positive values indicate leads which extend below package backside.

APPENDIX A: DOCUMENT REVISION HISTORY

Rev A Document (05/2023)

Initial release of this document.

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