

Silicon Errata and Data Sheet Clarification

The PAC1710 and PAC1720 parts you have received conform functionally to the PAC1710/20 Data Sheet (DS20005386), except for the anomalies described below.

The silicon issues discussed in the following pages are for silicon revisions with the Product and Revision IDs listed in [Table 1](#). This silicon's issues are summarized in [Table 2](#).

Future device revisions will address the issues noted.

TABLE 1: SILICON REVISION TABLE

Part Number	Product ID	Revision ID	Silicon Revision
PAC1720	57h	81h	A2
PAC1710	58h	81h	A2

Note 1: The Product ID and Revision ID are located in the registers `FDh` and `FFh` respectively, as seen in the PAC1710/20 Data Sheet.

TABLE 2: SILICON ISSUE SUMMARY

Module Number	Module Name	Feature	Issue Summary	Affected Revision
1	Standby Mode	Standby	Device may not respond after going into Standby mode.	A2

1. Module: Standby Mode

PAC1710/20 allows disabling V_{BUS} and/or V_{SENSE} to reduce current consumption. After V_{BUS} and/or V_{SENSE} measurements are disabled, PAC1710/20 may not be able to re-enable the measurements. The result is that new measurements are not taken.

Work around 1

A 300 ms delay is required after changing the Configuration Register (`00h`). After the delay, normal communication can occur.

Work around 2

Enable the Conversion Done Enable (CDEN) alert in the Configuration Register (`00h`). After the conversion is complete, communication with PAC1710/20 is available. This option is best suited for real-time applications.

Work around 3

POR PAC1710/20. The device restarts in the default condition and communicates normally.

APPENDIX A: DOCUMENT REVISION HISTORY

Revision B (September 2024)

- Updated [Table 2, Silicon Issue Summary](#).
- Updated **Modules** section. Following issues were fixed:
 - **V_{BUS}** module – ADC Measurement
Issue Summary: When temperatures drop below +25°C, V_{BUS} measurements may be incorrect.
 - **I²C Address** module – I²C Addressing
Issue Summary: I²C address can be incorrect when POR occurs below +25°C.

Revision A (October 2021)

- Initial release of this document, issued for silicon A2.

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