



SC-88A (SC-70-5/SOT-353) CASE 419A-02 ISSUE M

DATE 11 APR 2023

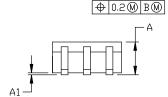
NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- 2. CONTROLLING DIMENSION: MILLIMETERS
- 419A-01 DBSDLETE. NEW STANDARD 419A-02
- DIMENSIONS D AND E1 DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS. MOLD FLASH, PROTRUSIONS, OR GATE BURRS SHALL NOT EXCEED 0.1016MM PER SIDE.

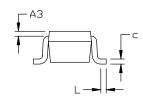
DIM	MILLIMETERS		
	MIN.	N□M.	MAX.
А	0.80	0.95	1.10
A1			0.10
A3	0.20 REF		
b	0.10	0.20	0.30
С	0.10		0.25
D	1.80	2.00	2,20
Е	2.00	2.10	2,20
E1	1.15	1.25	1.35
е	0.65 BSC		
L	0.10	0.15	0.30

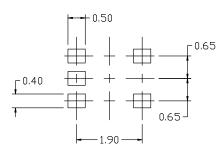
е Ε1

0



5X b





RECOMMENDED MOUNTING FOOTPRINT

For additional information on our Pb-Free strategy and soldering details, please download the DN Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

GENERIC MARKING DIAGRAM*



*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "■", may or may not be present. Some products may not follow the Generic Marking.

XXX = Specific Device Code

= Date Code

= Pb-Free Package

(Note: Microdot may be in either location)

STYLE 1:
PIN 1. BASE
EMITTER
3. BASE
COLLECTOR
COLLECTOR

PIN 1. EMITTER 2

STYLE 6:

STYLE 2: PIN 1. ANODE 2. EMITTER 3. BASE 4. COLLECTOR CATHODE

STYLE 3: PIN 1. ANODE 1 2. N/C 3. ANODE 2 4. CATHODE 2 5. CATHODE 1

STYLE 4: PIN 1. SOURCE 1 2. DRAIN 1/2 3 SOURCE 1 4. GATE 1 5. GATE 2

STYLE 5: PIN 1. CATHODE 2. COMMON ANODE

3. CATHODE 2 4. CATHODE 3 5. CATHODE 4

2. BASE 2 3. EMITTER 1 COLLECTOR 5. COLLECTOR 2/BASE 1 STYLE 7: PIN 1. BASE 2. EMITTER 3. BASE 4. COLLECTOR 5. COLLECTOR STYLE 8: PIN 1. CATHODE 2. COLLECTOR 3. N/C 4. BASE

5. EMITTER

PIN 1. ANODE 2. CATHODE 3. ANODE 4. ANODE 5 ANODE

STYLE 9:

Note: Please refer to datasheet for style callout. If style type is not called out in the datasheet refer to the device datasheet pinout or pin assignment.

DOCUMENT NUMBER:

98ASB42984B

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