

JFET - VHF/UHF Amplifier Transistor

N-Channel

MMBFJ309L, MMBFJ310L, SMMBFJ309L, SMMBFJ309L

Features

- Drain and Source are Interchangeable
- S Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Drain-Source Voltage	V _{DS}	25	Vdc
Gate-Source Voltage	V _{GS}	25	Vdc
Gate Current	IG	10	mAdc

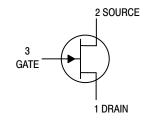
THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation FR-5 Board, (Note 1) T _A = 25°C Derate above 25°C	P _D	225 1.8	mW mW/°C
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	556	°C/W
Junction and Storage Temperature	T _J , T _{stg}	-55 to +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1

1. $FR-5 = 1.0 \times 0.75 \times 0.062$ in.





SOT-23 (TO-236) CASE 318 STYLE 10

MARKING DIAGRAM



6x = Device Code

x = U for MMBFJ309L, SMMBFJ309L x = T for MMBFJ310L, SMMBFJ310L

M = Date Code*

= Pb-Free Package

(Note: Microdot may be in either location)

*Date Code orientation and/or overbar may vary depending upon manufacturing location.

ORDERING INFORMATION

Device	Package	Shipping [†]
MMBFJ309LT1G,	SOT-23	3,000 / Tape &
SMMBFJ309LT1G	(Pb-Free)	Reel
MMBFJ310LT1G,	SOT-23	3,000 / Tape &
SMMBFJ310LT1G	(Pb-Free)	Reel
SMMBFJ310LT3G	SOT-23 (Pb-Free)	10,000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

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ELECTRICAL CHARACTERISTICS ($T_A = 25$ °C unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS					
Gate–Source Breakdown Voltage $(I_G = -1.0 \mu Adc, V_{DS} = 0)$	V _{(BR)GSS}	-25	_	_	Vdc
Gate Reverse Current (V _{GS} = -15 Vdc) (V _{GS} = -15 Vdc, T _A = 125°C)	I _{GSS}	_ _	- -	-1.0 -1.0	nAdc μAdc
Gate Source Cutoff Voltage MMBFJ309 (V _{DS} = 10 Vdc, I _D = 1.0 nAdc) MMBFJ310, SMMBFJ310	· GO(011)	-1.0 -2.0	- -	-4.0 -6.5	Vdc
ON CHARACTERISTICS	•	•	•	•	•
	D00	12 24	- -	30 60	mAdc
Gate-Source Forward Voltage (I _G = 1.0 mAdc, V _{DS} = 0)	V _{GS(f)}	_	-	1.0	Vdc
SMALL-SIGNAL CHARACTERISTICS					
Forward Transfer Admittance (V _{DS} = 10 Vdc, I _D = 10 mAdc, f = 1.0 kHz)	Y _{fs}	8.0	-	18	mmhos
Output Admittance (V _{DS} = 10 Vdc, I _D = 10 mAdc, f = 1.0 kHz)	y _{os}	-	-	250	μmhos
Input Capacitance (V _{GS} = -10 Vdc, V _{DS} = 0 Vdc, f = 1.0 MHz)	C _{iss}	-	-	5.0	pF
Reverse Transfer Capacitance (V _{GS} = -10 Vdc, V _{DS} = 0 Vdc, f = 1.0 MHz)	C _{rss}	-	_	2.5	pF
Equivalent Short-Circuit Input Noise Voltage (V _{DS} = 10 Vdc, I _D = 10 mAdc, f = 100 Hz)	e _n	-	10	-	nV/√Hz

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

MMBFJ309L, MMBFJ310L, SMMBFJ309L, SMMBFJ310L

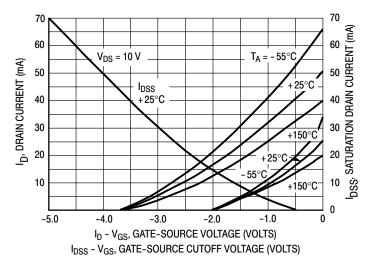


Figure 1. Drain Current and Transfer Characteristics versus Gate-Source Voltage

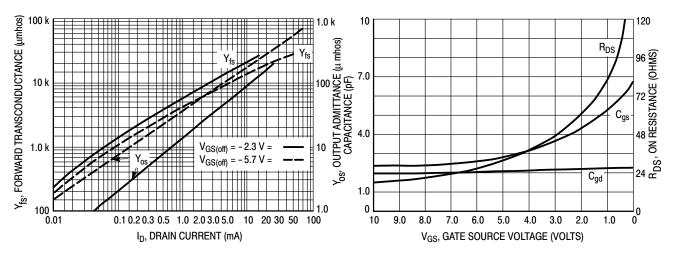


Figure 2. Common-Source Output
Admittance and Forward Transconductance
versus Drain Current

Figure 3. On Resistance and Junction Capacitance versus Gate-Source Voltage

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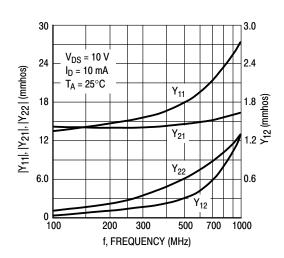


Figure 4. Common-Gate Y Parameter Magnitude versus Frequency

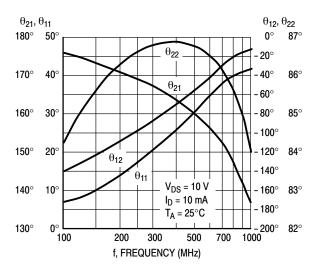


Figure 6. Common–Gate Y Parameter Phase–Angle versus Frequency

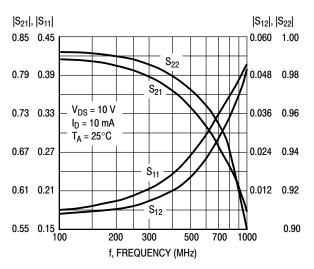


Figure 5. Common-Gate S Parameter Magnitude versus Frequency

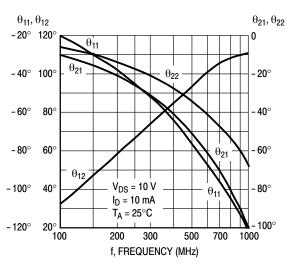


Figure 7. S Parameter Phase-Angle versus Frequency

MILLIMETERS

MIN

0.89

0.01

0.37

0.08

2.80

1.20

1.78

0.30

0.35

2.10

O°

NOM

1.00

0.06

0.44

0.14

2.90

1.30

1.90

0.43

0.54

2.40





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MAX

1.11

0.10

0.50

0.20

3.04

1.40

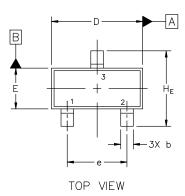
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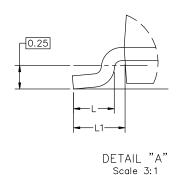
0.55

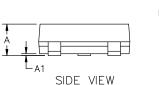
0.69

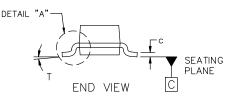
2.64

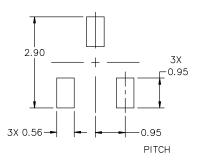
10°











NOTES:

DIM

Α

Α1

b

С

D

Ε

е L

L1

HE

Τ

- DIMENSIONING AND TOLERANCING 1. PER ASME Y14.5M, 2018. CONTROLLING DIMENSIONS:
- MILLIMETERS.
- MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF THE
- BASE MATERIAL.
 DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS.

GENERIC MARKING DIAGRAM*



XXX = Specific Device Code

= Date Code

= Pb-Free Package



* For additional information on our Pb-Free strategy and soldering details, please download the onsemi Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

STYLES ON PAGE 2

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^{*}This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "=", may or may not be present. Some products may not follow the Generic Marking.

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DATE 14 AUG 2024

STYLE 1 THRU 5: CANCELLED	STYLE 6: PIN 1. BASE 2. EMITTER 3. COLLECTOR	STYLE 7: PIN 1. EMITTER 2. BASE 3. COLLECTOR	STYLE 8: PIN 1. ANODE 2. NO CONNECTION 3. CATHODE	1	
STYLE 9:	STYLE 10:	STYLE 11:	STYLE 12: PIN 1. CATHODE 2. CATHODE 3. ANODE	STYLE 13:	STYLE 14:
PIN 1. ANODE	PIN 1. DRAIN	PIN 1. ANODE		PIN 1. SOURCE	PIN 1. CATHODE
2. ANODE	2. SOURCE	2. CATHODE		2. DRAIN	2. GATE
3. CATHODE	3. GATE	3. CATHODE-ANODE		3. GATE	3. ANODE
STYLE 15:	STYLE 16:	STYLE 17:	STYLE 18:	STYLE 19:	STYLE 20: PIN 1. CATHODE 2. ANODE 3. GATE
PIN 1. GATE	PIN 1. ANODE	PIN 1. NO CONNECTION	PIN 1. NO CONNECTION	N PIN 1. CATHODE	
2. CATHODE	2. CATHODE	2. ANODE	2. CATHODE	2. ANODE	
3. ANODE	3. CATHODE	3. CATHODE	3. ANODE	3. CATHODE-ANODI	
STYLE 21:	STYLE 22:	STYLE 23:	STYLE 24:	STYLE 25:	STYLE 26:
PIN 1. GATE	PIN 1. RETURN	PIN 1. ANODE	PIN 1. GATE	PIN 1. ANODE	PIN 1. CATHODE
2. SOURCE	2. OUTPUT	2. ANODE	2. DRAIN	2. CATHODE	2. ANODE
3. DRAIN	3. INPUT	3. CATHODE	3. SOURCE	3. GATE	3. NO CONNECTION
STYLE 27: PIN 1. CATHODE 2. CATHODE 3. CATHODE	STYLE 28: PIN 1. ANODE 2. ANODE 3. ANODE				

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