

# Switch-mode NPN Bipolar Power Transistor

For Switching Power Supply Applications

## MJE18008

The MJE18008 have an applications specific state-of-the-art die designed for use in 220 V line-operated switch-mode Power supplies and electronic light ballasts.

### Features

- Improved Efficiency Due to Low Base Drive Requirements:
  - High and Flat DC Current Gain  $h_{FE}$
  - Fast Switching
  - No Coil Required in Base Circuit for Turn-Off (No Current Tail)
- Tight Parametric Distributions are Consistent Lot-to-Lot
- Two Package Choices: Standard TO-220 or Isolated TO-220
- These Devices are Pb-Free and are RoHS Compliant\*

### MAXIMUM RATINGS

| Rating                                                                                                                                                                      | Symbol         | Value                | Unit                     |
|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------|----------------|----------------------|--------------------------|
| Collector-Emitter Sustaining Voltage                                                                                                                                        | $V_{CEO}$      | 450                  | Vdc                      |
| Collector-Base Breakdown Voltage                                                                                                                                            | $V_{CES}$      | 1000                 | Vdc                      |
| Emitter-Base Voltage                                                                                                                                                        | $V_{EBO}$      | 9.0                  | Vdc                      |
| Collector Current – Continuous                                                                                                                                              | $I_C$          | 8.0                  | Adc                      |
| Collector Current – Peak (Note 1)                                                                                                                                           | $I_{CM}$       | 16                   | Adc                      |
| Base Current – Continuous                                                                                                                                                   | $I_B$          | 4.0                  | Adc                      |
| Base Current – Peak (Note 1)                                                                                                                                                | $I_{BM}$       | 8.0                  | Adc                      |
| RMS Isolation Voltage (Note 2)<br>Test No. 1 Per Figure 22a<br>Test No. 1 Per Figure 22b<br>Test No. 1 Per Figure 22c<br>(for 1 sec, R.H. < 30%, $T_A = 25^\circ\text{C}$ ) | $V_{ISOL}$     | 4500<br>3500<br>1500 | V                        |
| Total Device Dissipation @ $T_C = 25^\circ\text{C}$<br>Derate above $25^\circ\text{C}$                                                                                      | $P_D$          | 125<br>1.0           | W<br>W/ $^\circ\text{C}$ |
| Operating and Storage Temperature                                                                                                                                           | $T_J, T_{stg}$ | -65 to 150           | $^\circ\text{C}$         |

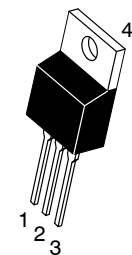
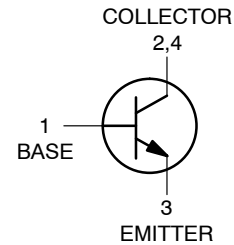
### THERMAL CHARACTERISTICS

| Characteristics                                                              | Symbol          | Max  | Unit                      |
|------------------------------------------------------------------------------|-----------------|------|---------------------------|
| Thermal Resistance, Junction-to-Case                                         | $R_{\theta JC}$ | 1.0  | $^\circ\text{C}/\text{W}$ |
| Thermal Resistance, Junction-to-Ambient                                      | $R_{\theta JA}$ | 62.5 | $^\circ\text{C}/\text{W}$ |
| Maximum Lead Temperature for Soldering Purposes 1/8" from Case for 5 Seconds | $T_L$           | 260  | $^\circ\text{C}$          |

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

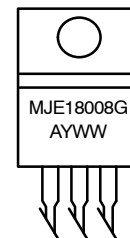
- Pulse Test: Pulse Width = 5 ms, Duty Cycle  $\leq 10\%$ .
- Proper strike and creepage distance must be provided.

POWER TRANSISTOR  
 8.0 AMPERES  
 1000 VOLTS  
 45 and 125 WATTS



TO-220AB  
 CASE 221A-09  
 STYLE 1

### MARKING DIAGRAM



G = Pb-Free Package  
 A = Assembly Location  
 Y = Year  
 WW = Work Week

### ORDERING INFORMATION

| Device    | Package               | Shipping        |
|-----------|-----------------------|-----------------|
| MJE18008G | TO-220AB<br>(Pb-Free) | 50 Units / Rail |

\*For additional information on our Pb-Free strategy and soldering details, please download the **onsemi** Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

**ELECTRICAL CHARACTERISTICS** ( $T_C = 25^\circ\text{C}$  unless otherwise specified)

| Characteristic | Symbol | Min | Typ | Max | Unit |
|----------------|--------|-----|-----|-----|------|
|----------------|--------|-----|-----|-----|------|

**OFF CHARACTERISTICS**

|                                                                                     |                |     |   |     |                 |
|-------------------------------------------------------------------------------------|----------------|-----|---|-----|-----------------|
| Collector–Emitter Sustaining Voltage ( $I_C = 100\text{ mA}$ , $L = 25\text{ mH}$ ) | $V_{CEO(sus)}$ | 450 | – | –   | Vdc             |
| Collector Cutoff Current ( $V_{CE} = \text{Rated } V_{CEO}$ , $I_B = 0$ )           | $I_{CEO}$      | –   | – | 100 | $\mu\text{Adc}$ |
| Collector Cutoff Current ( $V_{CE} = \text{Rated } V_{CES}$ , $V_{EB} = 0$ )        | $I_{CES}$      | –   | – | 100 | $\mu\text{Adc}$ |
| ( $T_C = 125^\circ\text{C}$ )                                                       |                | –   | – | 500 |                 |
| ( $V_{CE} = 800\text{ V}$ , $V_{EB} = 0$ ) ( $T_C = 125^\circ\text{C}$ )            |                | –   | – | 100 |                 |
| Emitter Cutoff Current ( $V_{EB} = 9.0\text{ Vdc}$ , $I_C = 0$ )                    | $I_{EBO}$      | –   | – | 100 | $\mu\text{Adc}$ |

**ON CHARACTERISTICS**

|                                                                                                                                            |               |        |              |             |     |
|--------------------------------------------------------------------------------------------------------------------------------------------|---------------|--------|--------------|-------------|-----|
| Base–Emitter Saturation Voltage ( $I_C = 2.0\text{ Adc}$ , $I_B = 0.2\text{ Adc}$ )<br>( $I_C = 4.5\text{ Adc}$ , $I_B = 0.9\text{ Adc}$ ) | $V_{BE(sat)}$ | –<br>– | 0.82<br>0.92 | 1.1<br>1.25 | Vdc |
| Collector–Emitter Saturation Voltage<br>( $I_C = 2.0\text{ Adc}$ , $I_B = 0.2\text{ Adc}$ )                                                | $V_{CE(sat)}$ | –      | 0.3          | 0.6         | Vdc |
| ( $T_C = 125^\circ\text{C}$ )                                                                                                              |               | –      | 0.3          | 0.65        |     |
| ( $I_C = 4.5\text{ Adc}$ , $I_B = 0.9\text{ Adc}$ )                                                                                        |               | –      | 0.35         | 0.7         |     |
| ( $T_C = 125^\circ\text{C}$ )                                                                                                              |               | –      | 0.4          | 0.8         |     |
| DC Current Gain ( $I_C = 1.0\text{ Adc}$ , $V_{CE} = 5.0\text{ Vdc}$ )                                                                     | $h_{FE}$      | 14     | –            | 34          | –   |
| ( $T_C = 125^\circ\text{C}$ )                                                                                                              |               | –      | 28           | –           |     |
| ( $I_C = 4.5\text{ Adc}$ , $V_{CE} = 1.0\text{ Vdc}$ )                                                                                     |               | 6.0    | 9.0          | –           |     |
| ( $T_C = 125^\circ\text{C}$ )                                                                                                              |               | 5.0    | 8.0          | –           |     |
| ( $I_C = 2.0\text{ Adc}$ , $V_{CE} = 1.0\text{ Vdc}$ )                                                                                     |               | 11     | 15           | –           |     |
| ( $T_C = 125^\circ\text{C}$ )                                                                                                              |               | 11     | 16           | –           |     |
| ( $I_C = 10\text{ mAdc}$ , $V_{CE} = 5.0\text{ Vdc}$ )                                                                                     |               | 10     | 20           | –           |     |

**DYNAMIC CHARACTERISTICS**

|                                                                                                                                                                                              |                |                            |                                                         |                                      |     |
|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|----------------|----------------------------|---------------------------------------------------------|--------------------------------------|-----|
| Current Gain Bandwidth ( $I_C = 0.5\text{ Adc}$ , $V_{CE} = 10\text{ Vdc}$ , $f = 1.0\text{ MHz}$ )                                                                                          | $f_T$          | –                          | 13                                                      | –                                    | MHz |
| Output Capacitance ( $V_{CB} = 10\text{ Vdc}$ , $I_E = 0$ , $f = 1.0\text{ MHz}$ )                                                                                                           | $C_{ob}$       | –                          | 100                                                     | 150                                  | pF  |
| Input Capacitance ( $V_{EB} = 8.0\text{ V}$ )                                                                                                                                                | $C_{ib}$       | –                          | 1750                                                    | 2500                                 | pF  |
| Dynamic Saturation Voltage:<br>Determined $1.0\text{ }\mu\text{s}$ and<br>$3.0\text{ }\mu\text{s}$ respectively after<br>rising $I_{B1}$ reaches 90% of<br>final $I_{B1}$<br>(see Figure 18) | $V_{CE(dsat)}$ | –<br>–<br>–<br>–<br>–<br>– | 5.5<br>11.5<br>3.5<br>6.5<br>11.5<br>14.5<br>2.4<br>9.0 | –<br>–<br>–<br>–<br>–<br>–<br>–<br>– | Vdc |
| ( $I_C = 2.0\text{ Adc}$ , $I_{B1} = 200\text{ mAdc}$ , $V_{CC} = 300\text{ V}$ )                                                                                                            |                | 1.0 $\mu\text{s}$          | ( $T_C = 125^\circ\text{C}$ )                           |                                      |     |
|                                                                                                                                                                                              |                | 3.0 $\mu\text{s}$          | ( $T_C = 125^\circ\text{C}$ )                           |                                      |     |
| ( $I_C = 5.0\text{ Adc}$ , $I_{B1} = 1.0\text{ Adc}$ , $V_{CC} = 300\text{ V}$ )                                                                                                             |                | 1.0 $\mu\text{s}$          | ( $T_C = 125^\circ\text{C}$ )                           |                                      |     |
|                                                                                                                                                                                              |                | 3.0 $\mu\text{s}$          | ( $T_C = 125^\circ\text{C}$ )                           |                                      |     |

**SWITCHING CHARACTERISTICS: Resistive Load** (D.C.  $\leq 10\%$ , Pulse Width =  $20\text{ }\mu\text{s}$ )

|               |                                                                                                                  |                               |           |        |            |          |               |
|---------------|------------------------------------------------------------------------------------------------------------------|-------------------------------|-----------|--------|------------|----------|---------------|
| Turn–On Time  | ( $I_C = 2.0\text{ Adc}$ , $I_{B1} = 0.2\text{ Adc}$ ,<br>$I_{B2} = 1.0\text{ Adc}$ , $V_{CC} = 300\text{ V}$ )  | ( $T_C = 125^\circ\text{C}$ ) | $t_{on}$  | –<br>– | 200<br>190 | 300<br>– | ns            |
| Turn–Off Time |                                                                                                                  | ( $T_C = 125^\circ\text{C}$ ) | $t_{off}$ | –<br>– | 1.2<br>1.5 | 2.5<br>– | $\mu\text{s}$ |
| Turn–On Time  | ( $I_C = 4.5\text{ Adc}$ , $I_{B1} = 0.9\text{ Adc}$ ,<br>$I_{B2} = 2.25\text{ Adc}$ , $V_{CC} = 300\text{ V}$ ) | ( $T_C = 125^\circ\text{C}$ ) | $t_{on}$  | –<br>– | 100<br>250 | 180<br>– | ns            |
| Turn–Off Time |                                                                                                                  | ( $T_C = 125^\circ\text{C}$ ) | $t_{off}$ | –<br>– | 1.6<br>2.0 | 2.5<br>– | $\mu\text{s}$ |

**SWITCHING CHARACTERISTICS: Inductive Load** ( $V_{clamp} = 300\text{ V}$ ,  $V_{CC} = 15\text{ V}$ ,  $L = 200\text{ }\mu\text{H}$ )

|                |                                                                                        |                               |          |        |            |           |               |
|----------------|----------------------------------------------------------------------------------------|-------------------------------|----------|--------|------------|-----------|---------------|
| Fall Time      | ( $I_C = 2.0\text{ Adc}$ , $I_{B1} = 0.2\text{ Adc}$ ,<br>$I_{B2} = 1.0\text{ Adc}$ )  | ( $T_C = 125^\circ\text{C}$ ) | $t_{fi}$ | –<br>– | 100<br>120 | 180<br>–  | ns            |
| Storage Time   |                                                                                        | ( $T_C = 125^\circ\text{C}$ ) | $t_{si}$ | –<br>– | 1.5<br>1.9 | 2.75<br>– | $\mu\text{s}$ |
| Crossover Time |                                                                                        | ( $T_C = 125^\circ\text{C}$ ) | $t_c$    | –<br>– | 250<br>230 | 350<br>–  | ns            |
| Fall Time      | ( $I_C = 4.5\text{ Adc}$ , $I_{B1} = 0.9\text{ Adc}$ ,<br>$I_{B2} = 2.25\text{ Adc}$ ) | ( $T_C = 125^\circ\text{C}$ ) | $t_{fi}$ | –<br>– | 85<br>135  | 150<br>–  | ns            |
| Storage Time   |                                                                                        | ( $T_C = 125^\circ\text{C}$ ) | $t_{si}$ | –<br>– | 2.0<br>2.6 | 3.2<br>–  | $\mu\text{s}$ |
| Crossover Time |                                                                                        | ( $T_C = 125^\circ\text{C}$ ) | $t_c$    | –<br>– | 210<br>250 | 300<br>–  | ns            |

- Pulse Test: Pulse Width =  $5.0\text{ ms}$ , Duty Cycle  $\leq 10\%$ .
- Proper strike and creepage distance must be provided.

TYPICAL STATIC CHARACTERISTICS

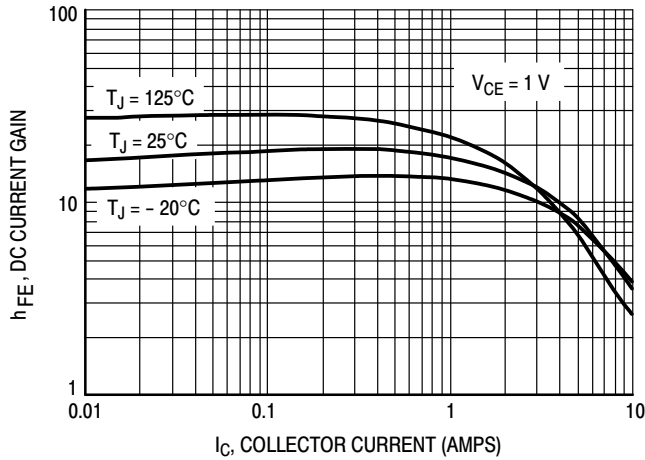


Figure 1. DC Current Gain @ 1 Volt

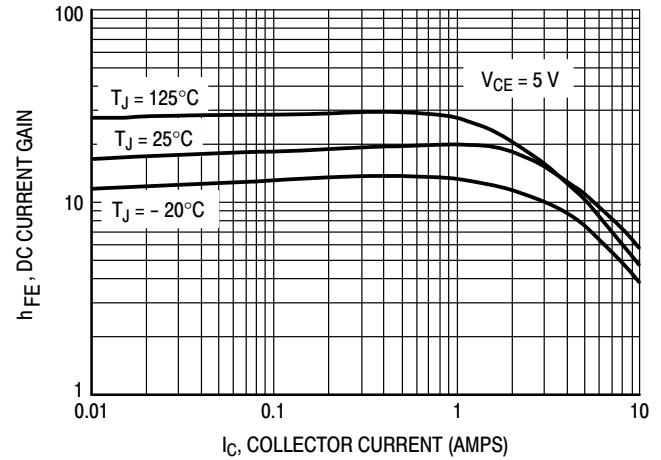


Figure 2. DC Current Gain @ 5 Volts

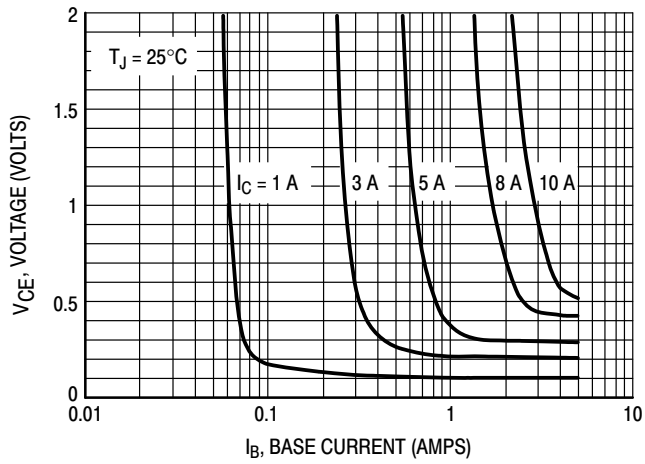


Figure 3. Collector Saturation Region

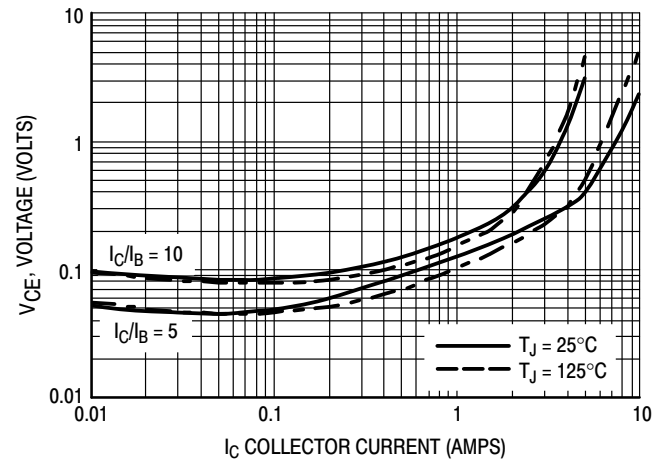


Figure 4. Collector-Emitter Saturation Voltage

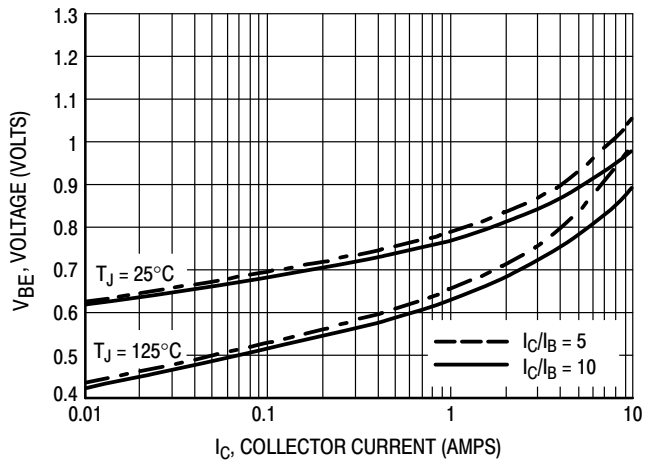


Figure 5. Base-Emitter Saturation Region

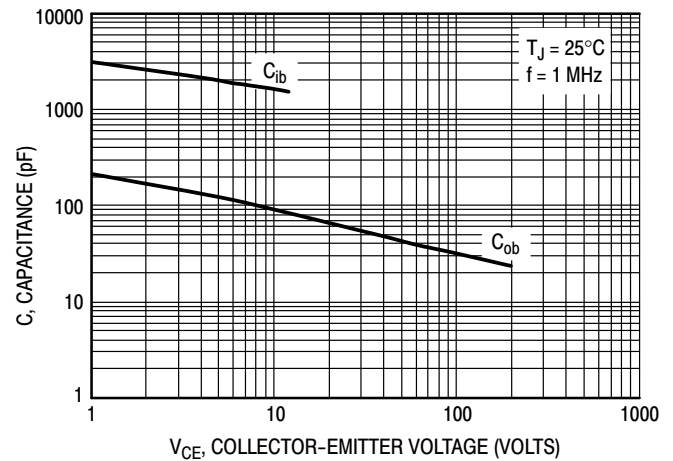


Figure 6. Capacitance

**TYPICAL SWITCHING CHARACTERISTICS**  
( $I_{B2} = I_C/2$  FOR ALL SWITCHING) (continued)

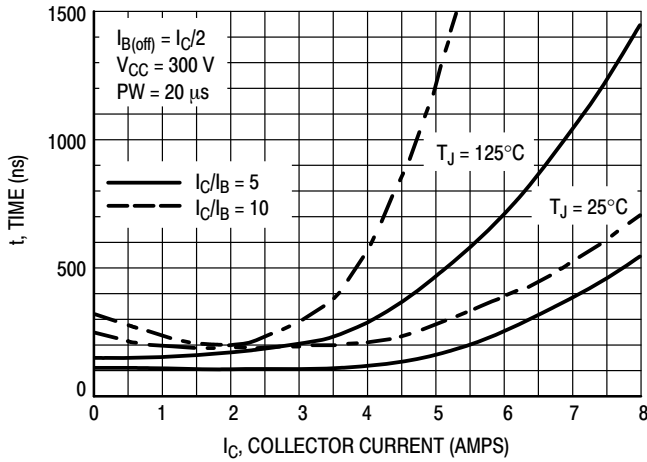


Figure 7. Resistive Switching,  $t_{on}$

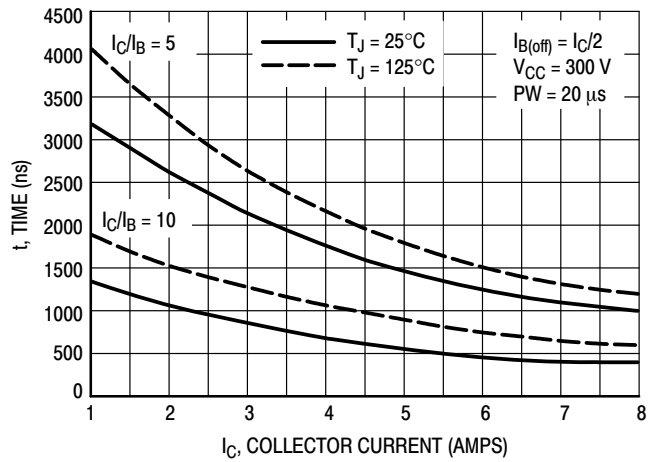


Figure 8. Resistive Switching,  $t_{off}$

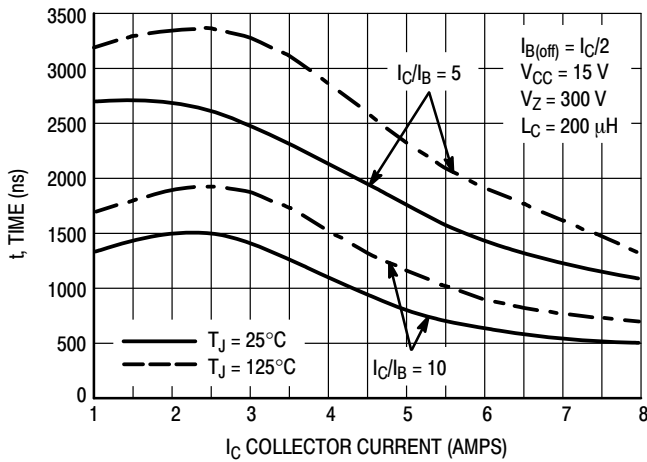


Figure 9. Inductive Storage Time,  $t_{si}$

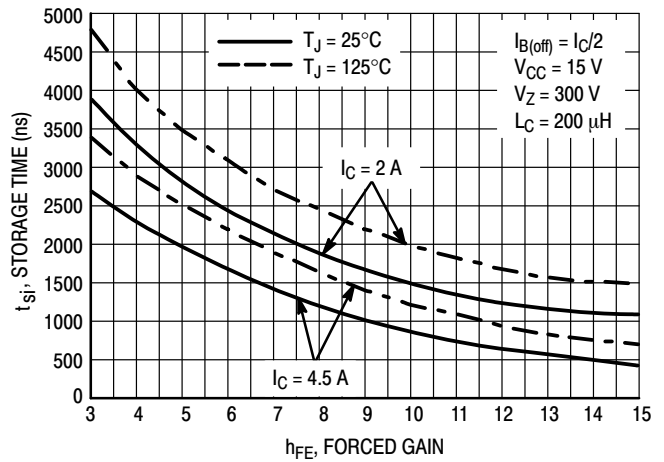


Figure 10. Inductive Storage Time,  $t_{si}(h_{FE})$

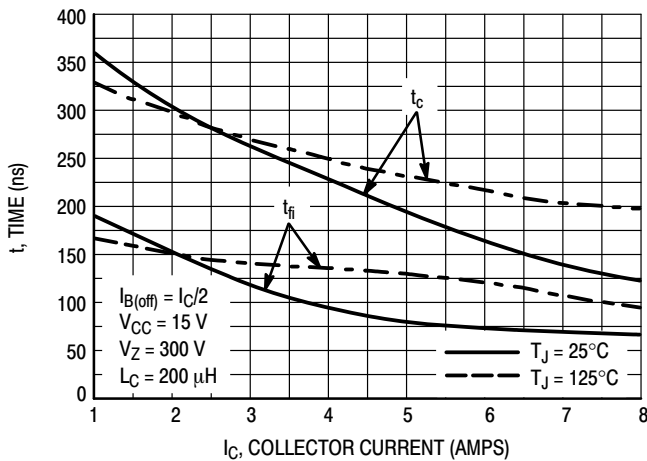


Figure 11. Inductive Switching,  $t_c$  and  $t_{fi}$   
 $I_C/I_B = 5$

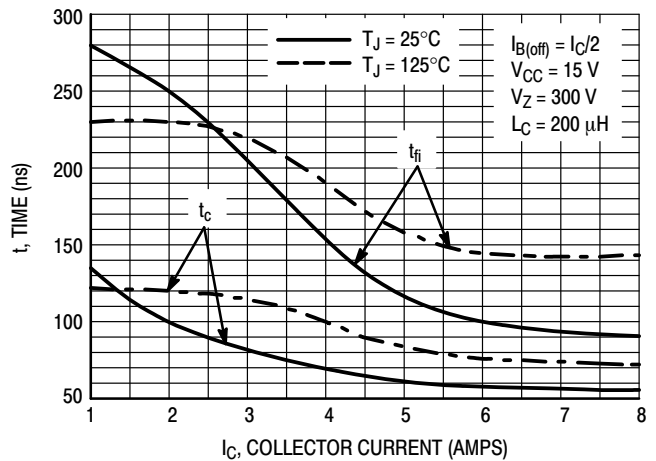


Figure 12. Inductive Switching,  $t_c$  and  $t_{fi}$   
 $I_C/I_B = 10$

# TYPICAL SWITCHING CHARACTERISTICS

( $I_{B2} = I_C/2$  for all switching) (continued)

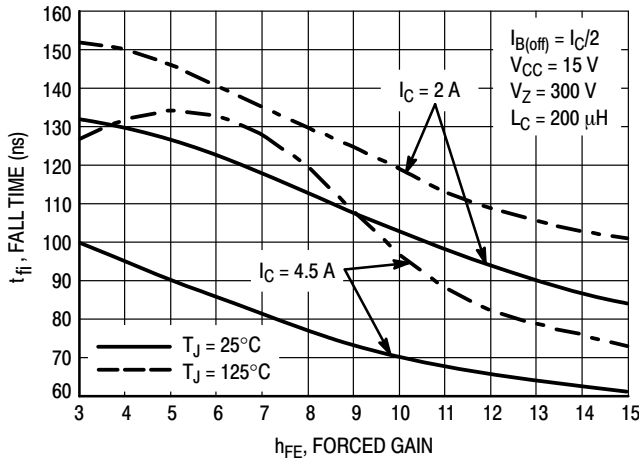


Figure 13. Inductive Fall Time

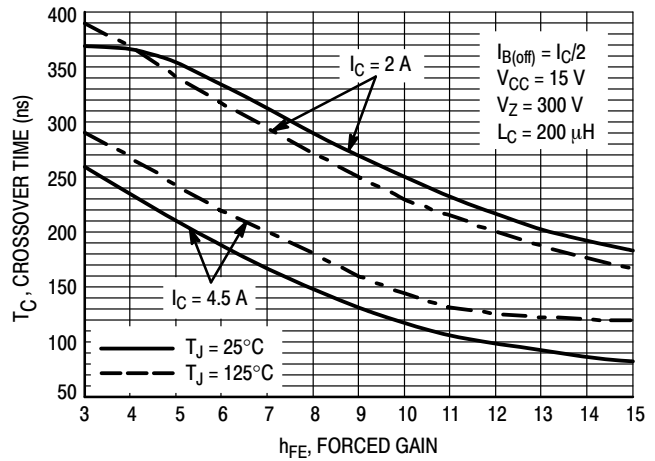


Figure 14. Inductive Crossover Time

## GUARANTEED SAFE OPERATING AREA INFORMATION

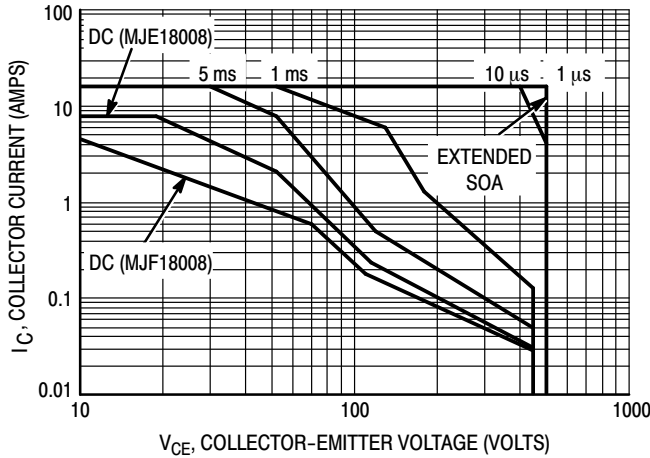


Figure 15. Forward Bias Safe Operating Area

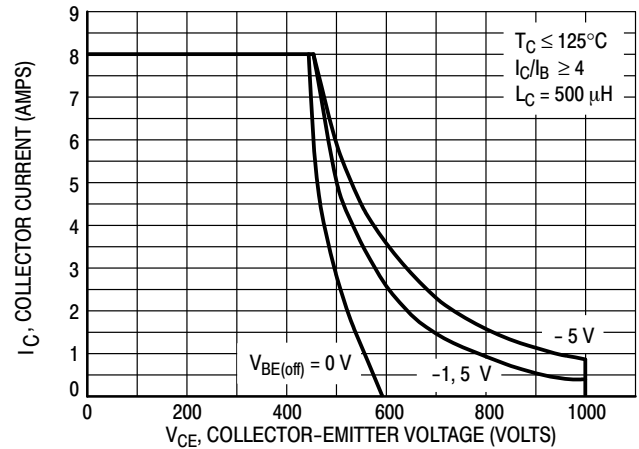


Figure 16. Reverse Bias Switching Safe Operating Area

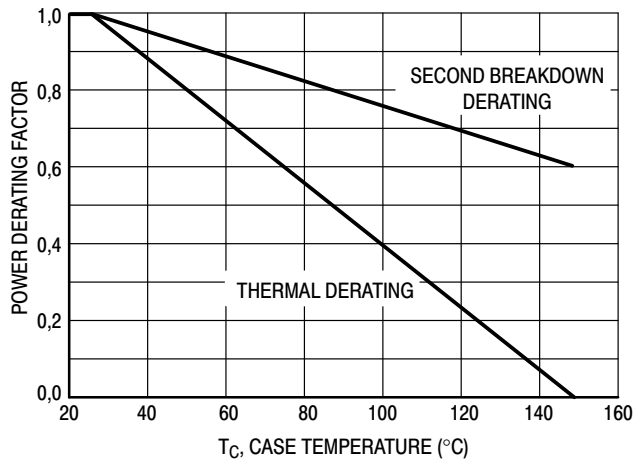


Figure 17. Forward Bias Power Derating

There are two limitations on the power handling ability of a transistor: average junction temperature and second breakdown. Safe operating area curves indicate  $I_C - V_{CE}$

limits of the transistor that must be observed for reliable operation; i.e., the transistor must not be subjected to greater dissipation than the curves indicate. The data of Figure 15 is based on  $T_C = 25^\circ\text{C}$ ;  $T_{J(pk)}$  is variable depending on power level. Second breakdown pulse limits are valid for duty cycles to 10% but must be derated when  $T_C > 25^\circ\text{C}$ . Second breakdown limitations do not derate the same as thermal limitations. Allowable current at the voltages shown in Figure 15 may be found at any case temperature by using the appropriate curve on Figure 17.  $T_{J(pk)}$  may be calculated from the data in Figure 20 and 21. At any case temperatures, thermal limitations will reduce the power that can be handled to values less than the limitations imposed by second breakdown. For inductive loads, high voltage and current must be sustained simultaneously during turn-off with the base-to-emitter junction reverse-biased. The safe level is specified as a reverse-biased safe operating area (Figure 16). This rating is verified under clamped conditions so that the device is never subjected to an avalanche mode.

# MJE18008

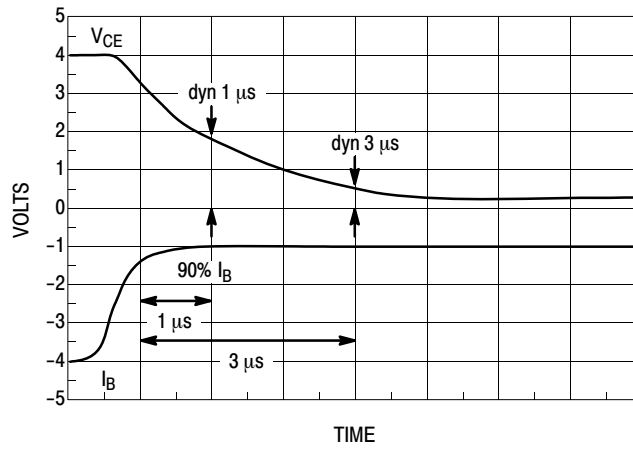


Figure 18. Dynamic Saturation Voltage Measurements

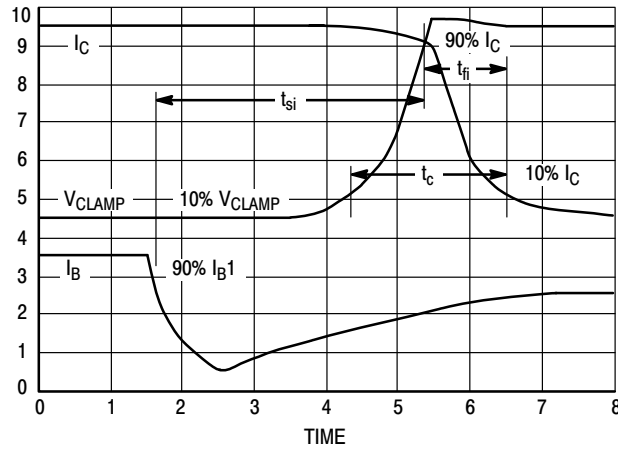


Figure 19. Inductive Switching Measurements

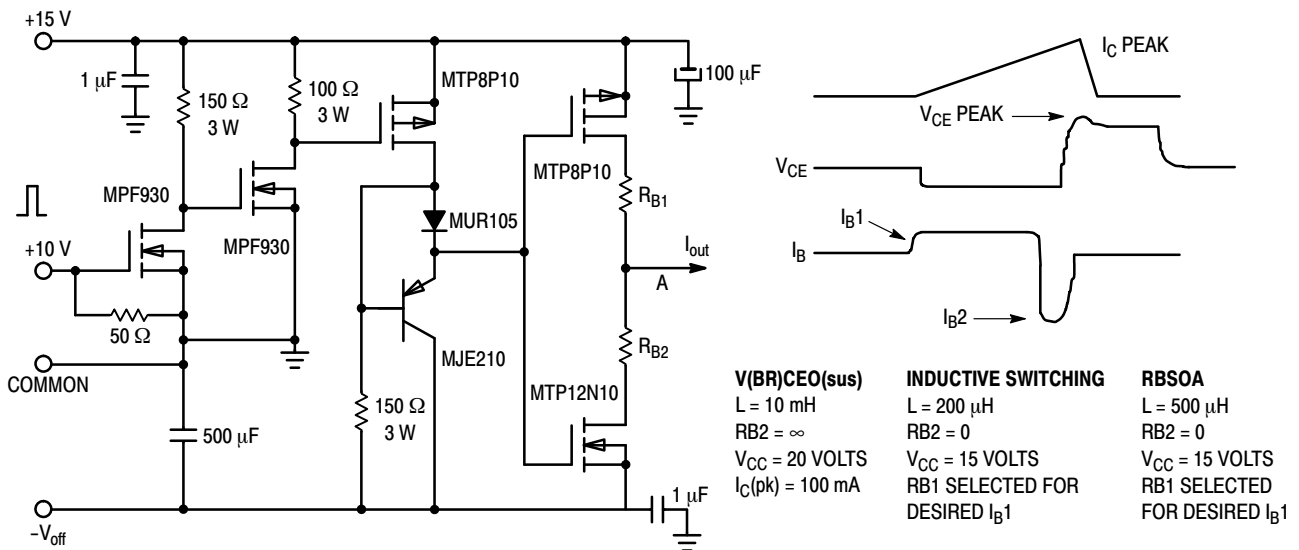


Table 1. Inductive Load Switching Drive Circuit

TYPICAL THERMAL RESPONSE

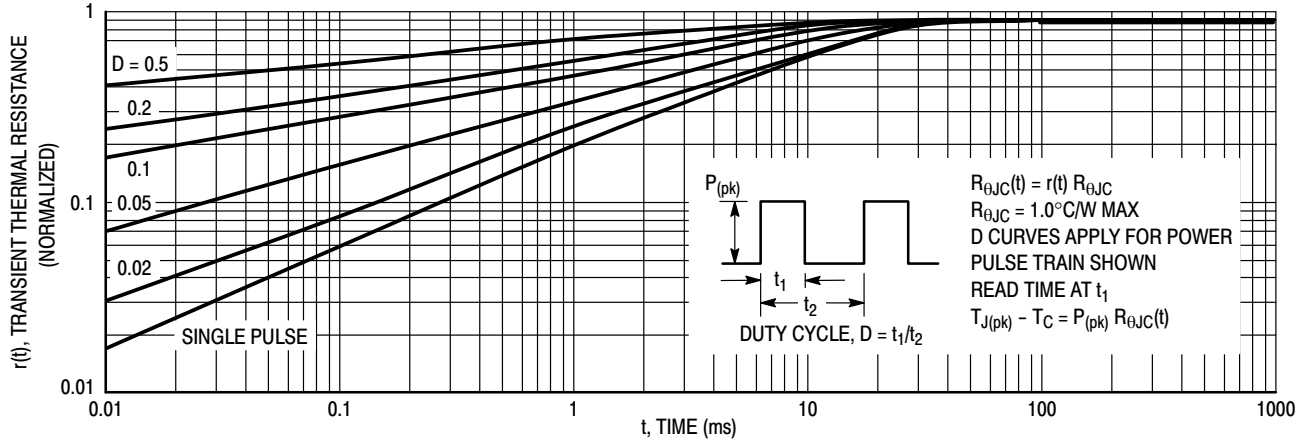


Figure 20. Typical Thermal Response ( $Z_{\theta_{JC}}(t)$ ) for MJE18008

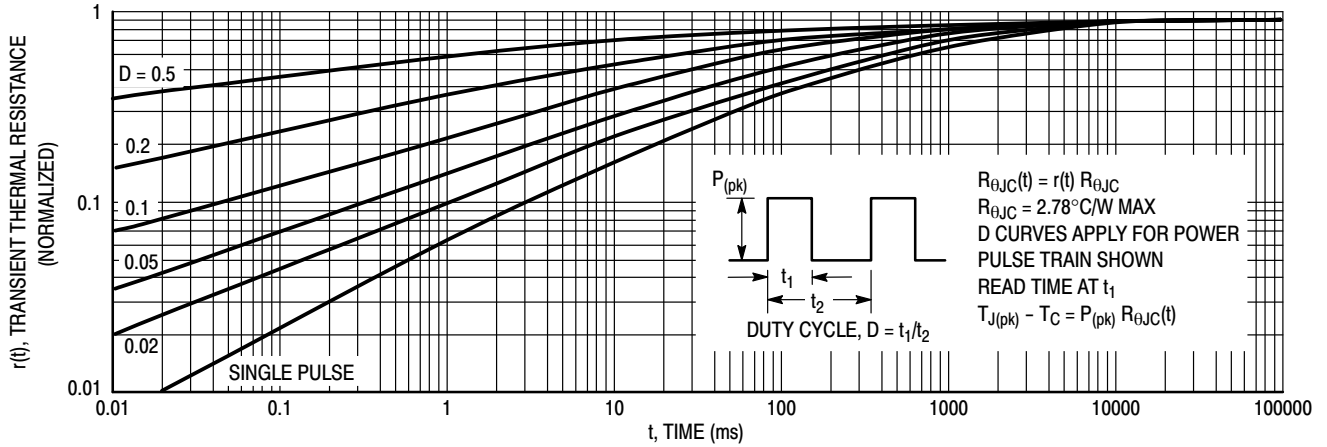


Figure 21. Typical Thermal Response ( $Z_{\theta_{JC}}(t)$ ) for MJF18008

TEST CONDITIONS FOR ISOLATION TESTS\*

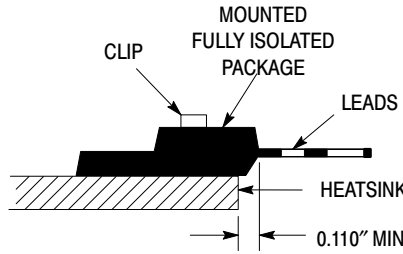


Figure 22a. Screw or Clip Mounting Position for Isolation Test Number 1

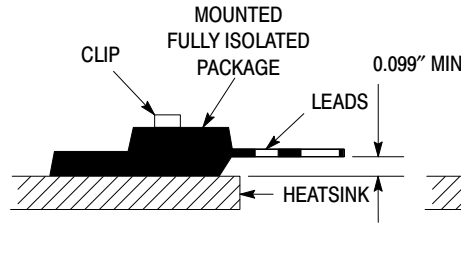


Figure 22b. Clip Mounting Position for Isolation Test Number 2

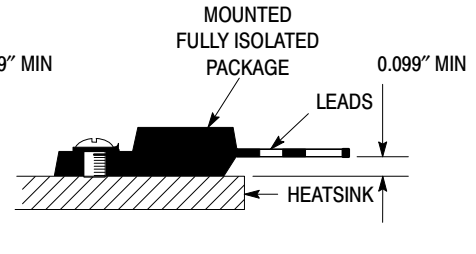


Figure 22c. Screw Mounting Position for Isolation Test Number 3

\*Measurement made between leads and heatsink with all leads shorted together

MOUNTING INFORMATION\*\*

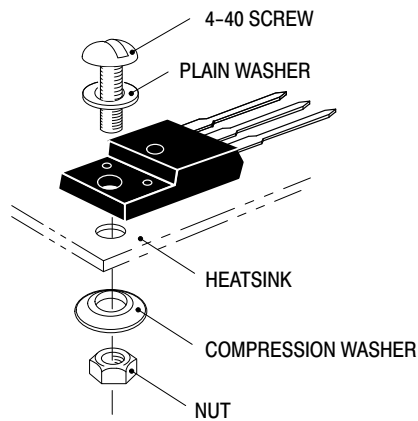


Figure 23a. Screw-Mounted

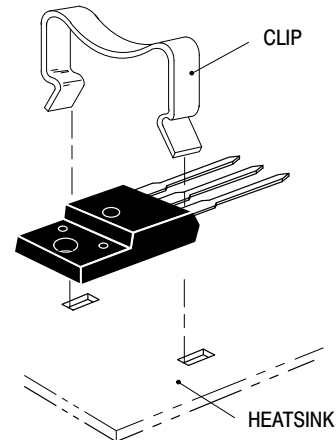


Figure 23b. Clip-Mounted

Figure 23. Typical Mounting Techniques for Isolated Package

Laboratory tests on a limited number of samples indicate, when using the screw and compression washer mounting technique, a screw torque of 6 to 8 in · lbs is sufficient to provide maximum power dissipation capability. The compression washer helps to maintain a constant pressure on the package over time and during large temperature excursions.

Destructive laboratory tests show that using a hex head 4-40 screw, without washers, and applying a torque in excess of 20 in · lbs will cause the plastic to crack around the mounting hole, resulting in a loss of isolation capability.

Additional tests on slotted 4-40 screws indicate that the screw slot fails between 15 to 20 in · lbs without adversely affecting the package. However, in order to positively ensure the package integrity of the fully isolated device, **onsemi** does not recommend exceeding 10 in · lbs of mounting torque under any mounting conditions.

\*\*For more information about mounting power semiconductors see Application Note AN1040.



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