

General Purpose Transistors

Voltage and Current are Negative for PNP Transistors

BCX17LT1G, PNP
BCX18LT1G, PNP
BCX19LT1G, NPN
SBCX19LT1G, NPN

Features

- S and NSV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

MAXIMUM RATINGS

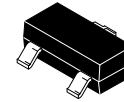
Symbol	Rating	Value	Unit
V_{CEO}	Collector – Emitter Voltage BCX17, BCX19 BCX18	45 25	Vdc
V_{CBO}	Collector – Base Voltage BCX17, BCX19 BCX18	50 30	Vdc
V_{EBO}	Emitter – Base Voltage	5.0	Vdc
I_C	Collector Current – Continuous	500	mAdc

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

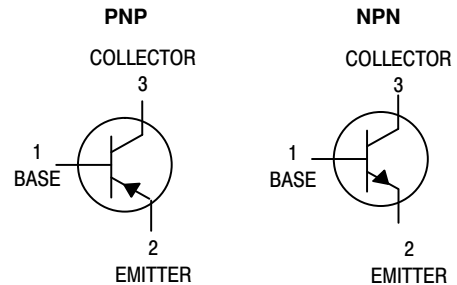
THERMAL CHARACTERISTICS

Symbol	Characteristic	Max	Unit
P_D	Total Device Dissipation FR-5 Board (Note 1), $T_A = 25^\circ\text{C}$ Derate above 25°C	225 1.8	mW mW/ $^\circ\text{C}$
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	556	$^\circ\text{C}/\text{W}$
P_D	Total Device Dissipation Alumina Substrate, (Note 2) $T_A = 25^\circ\text{C}$ Derate above 25°C	300 2.4	mW mW/ $^\circ\text{C}$
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	417	$^\circ\text{C}/\text{W}$
T_J, T_{stg}	Junction and Storage Temperature	-55 to +150	$^\circ\text{C}$

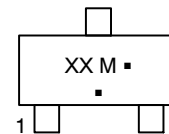
- FR-5 = $1.0 \times 0.75 \times 0.062$ in.
- Alumina = $0.4 \times 0.3 \times 0.024$ in 99.5% alumina.



SOT-23
(TO-236)
CASE 318
STYLE 6



MARKING DIAGRAM



XX = T1, T2 or U1
M = Date Code*
▪ = Pb-Free Package

(Note: Microdot may be in either location)

*Date Code orientation and/or overbar may vary depending upon manufacturing location.

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 4 of this data sheet.

NOTE: Some of the devices on this data sheet have been **DISCONTINUED**. Please refer to the table on page 4.

BCX17LT1G, PNP BCX18LT1G, PNP BCX19LT1G, NPN SBCX19LT1G, NPN

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Symbol	Characteristic	Min	Typ	Max	Unit
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OFF CHARACTERISTICS

V _{(BR)CEO}	Collector–Emitter Breakdown Voltage (I _C = 10 mAdc, I _B = 0) BCX17, BCX19, SBCX19 BCX18	45 25	– –	– –	Vdc
V _{(BR)CES}	Collector–Emitter Breakdown Voltage (I _C = 10 µAdc, I _C = 0) BCX17, BCX19, SBCX19 BCX18	50 30	– –	– –	Vdc
I _{CBO}	Collector Cutoff Current (V _{CB} = 20 Vdc, I _E = 0) (V _{CB} = 20 Vdc, I _E = 0, T _A = 150°C)	– –	– –	100 5.0	nAdc µAdc
I _{EBO}	Emitter Cutoff Current (V _{EB} = 5.0 Vdc, I _C = 0)	–	–	10	µAdc

ON CHARACTERISTICS

h _{FE}	DC Current Gain (I _C = 100 mAdc, V _{CE} = 1.0 Vdc) (I _C = 300 mAdc, V _{CE} = 1.0 Vdc) (I _C = 500 mAdc, V _{CE} = 1.0 Vdc)	100 70 40	– – –	600 – –	–
V _{CE(sat)}	Collector–Emitter Saturation Voltage (I _C = 500 mAdc, I _B = 50 mAdc)	–	–	0.62	Vdc
V _{BE(on)}	Base–Emitter On Voltage (I _C = 500 mAdc, V _{CE} = 1.0 Vdc)	–	–	1.2	Vdc

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

BCX17LT1G, PNP BCX18LT1G, PNP BCX19LT1G, NPN SBCX19LT1G, NPN

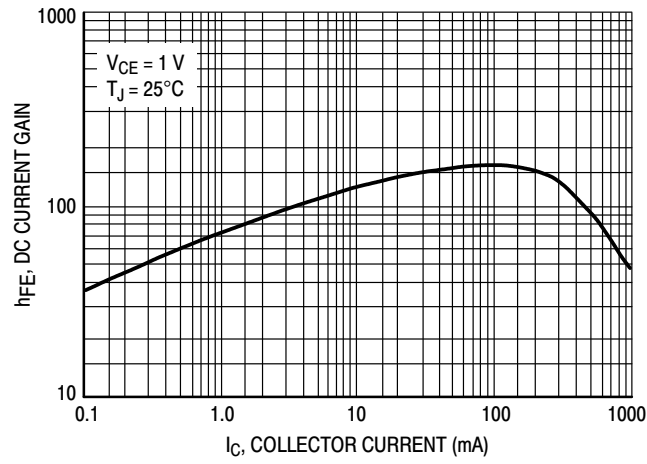


Figure 1. DC Current Gain

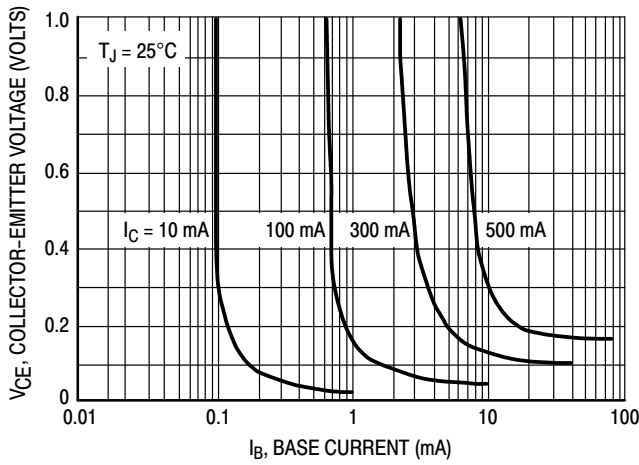


Figure 2. Saturation Region

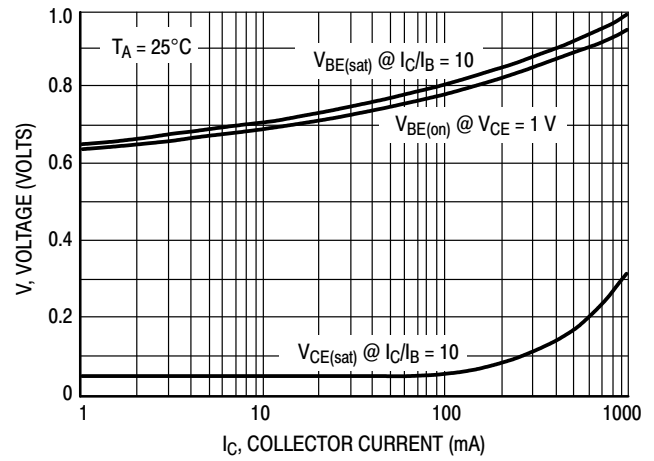


Figure 3. "On" Voltages

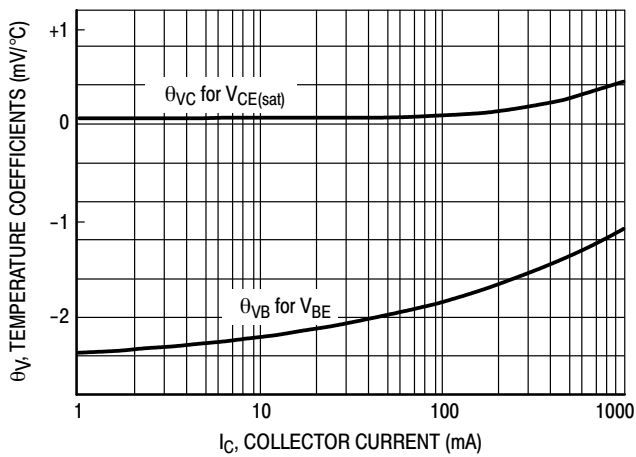


Figure 4. Temperature Coefficients

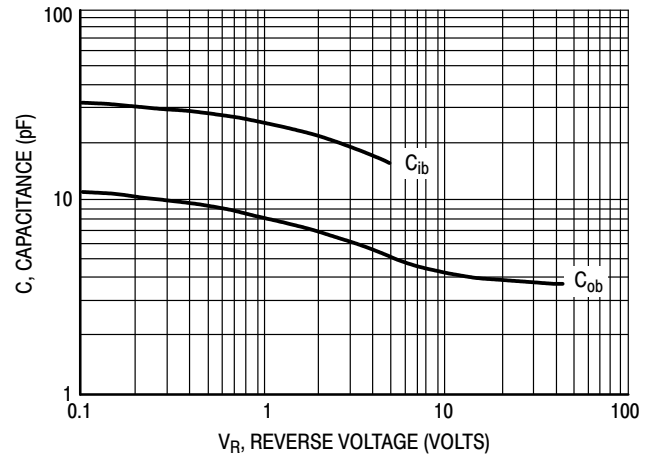


Figure 5. Capacitances

BCX17LT1G, PNP BCX18LT1G, PNP BCX19LT1G, NPN SBCX19LT1G, NPN

ORDERING INFORMATION

Device	Specific Marking	Package	Shipping†
BCX17LT1G	T1	SOT-23 (Pb-Free)	3,000 / Tape & Reel
NSVBCX17LT1G*	T1	SOT-23 (Pb-Free)	3,000 / Tape & Reel
BCX19LT1G	U1	SOT-23 (Pb-Free)	3,000 / Tape & Reel
SBCX19LT1G*	U1	SOT-23 (Pb-Free)	3,000 / Tape & Reel

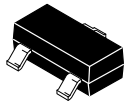
DISCONTINUED (Note 3)

BCX18LT1G	T2	SOT-23 (Pb-Free)	3,000 / Tape & Reel
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†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, [BRD8011/D](#).

*S and NSV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable.

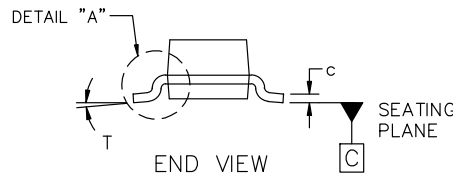
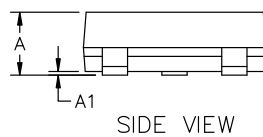
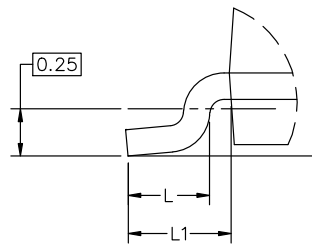
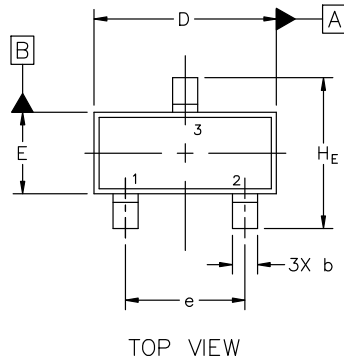
3. **DISCONTINUED:** This device is not recommended for new design. Please contact your **onsemi** representative for information. The most current information on this device may be available on www.onsemi.com.



SCALE 4:1

SOT-23 (TO-236) 2.90x1.30x1.00 1.90P
CASE 318
ISSUE AU

DATE 14 AUG 2024

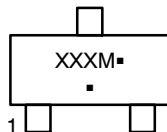


MILLIMETERS			
DIM	MIN	NOM	MAX
A	0.89	1.00	1.11
A1	0.01	0.06	0.10
b	0.37	0.44	0.50
c	0.08	0.14	0.20
D	2.80	2.90	3.04
E	1.20	1.30	1.40
e	1.78	1.90	2.04
L	0.30	0.43	0.55
L1	0.35	0.54	0.69
HE	2.10	2.40	2.64
T	0°	---	10°

NOTES:

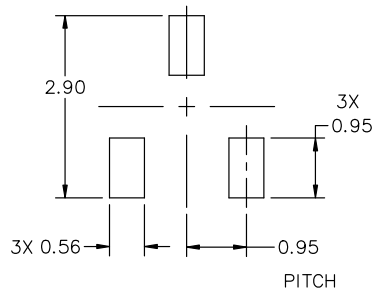
1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 2018.
2. CONTROLLING DIMENSIONS: MILLIMETERS.
3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF THE BASE MATERIAL.
4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS.

GENERIC MARKING DIAGRAM*



XXX = Specific Device Code
M = Date Code
▪ = Pb-Free Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present. Some products may not follow the Generic Marking.



RECOMMENDED MOUNTING FOOTPRINT

* For additional information on our Pb-Free strategy and soldering details, please download the onsemi Soldering and Mounting Techniques Reference Manual, SOLDERM/D.

STYLES ON PAGE 2

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STYLE 1 THRU 5: CANCELLED	STYLE 6: PIN 1. BASE 2. EMITTER 3. COLLECTOR	STYLE 7: PIN 1. EMITTER 2. BASE 3. COLLECTOR	STYLE 8: PIN 1. ANODE 2. NO CONNECTION 3. CATHODE		
STYLE 9: PIN 1. ANODE 2. ANODE 3. CATHODE	STYLE 10: PIN 1. DRAIN 2. SOURCE 3. GATE	STYLE 11: PIN 1. ANODE 2. CATHODE 3. CATHODE-ANODE	STYLE 12: PIN 1. CATHODE 2. CATHODE 3. ANODE	STYLE 13: PIN 1. SOURCE 2. DRAIN 3. GATE	STYLE 14: PIN 1. CATHODE 2. GATE 3. ANODE
STYLE 15: PIN 1. GATE 2. CATHODE 3. ANODE	STYLE 16: PIN 1. ANODE 2. CATHODE 3. CATHODE	STYLE 17: PIN 1. NO CONNECTION 2. ANODE 3. CATHODE	STYLE 18: PIN 1. NO CONNECTION 2. CATHODE 3. ANODE	STYLE 19: PIN 1. CATHODE 2. ANODE 3. CATHODE-ANODE	STYLE 20: PIN 1. CATHODE 2. ANODE 3. GATE
STYLE 21: PIN 1. GATE 2. SOURCE 3. DRAIN	STYLE 22: PIN 1. RETURN 2. OUTPUT 3. INPUT	STYLE 23: PIN 1. ANODE 2. ANODE 3. CATHODE	STYLE 24: PIN 1. GATE 2. DRAIN 3. SOURCE	STYLE 25: PIN 1. ANODE 2. CATHODE 3. GATE	STYLE 26: PIN 1. CATHODE 2. ANODE 3. NO CONNECTION
STYLE 27: PIN 1. CATHODE 2. CATHODE 3. CATHODE	STYLE 28: PIN 1. ANODE 2. ANODE 3. ANODE				

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